

M.S. O.I.P.E. SCANNED <u>KC4</u> Q.A. <u>UA</u>	PATENT DATE
--	-------------

APPLICATION NO. 09/892649	CONT/PRIOR D	CLASS 7 710 369	SUBCLASS 100 -43	ART UNIT., 101 2182 2651	EXAMINER PHAN, R
------------------------------	-----------------	-----------------------	------------------------	--------------------------------	---------------------

APPLICANTS

Siamack Nemazie
Kaushik Popat
Balaji Virajpet
William Foland

Time

Mixed-signal single-chip integrated system electronics for data storage devices

PTO-204
12/89[illegible]

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)		NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____ _____ _____	_____ (Primary Examiner) (Date)		ISSUE FEE	
			Amount Due	Date Paid
<input type="checkbox"/> The terminal ____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) (Date)		ISSUE BATCH NUMBER	

The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368. Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.

FILED WITH: ☐ DISK (CRF) ☐ FICHE ☐ CD-ROM
(Attached in pocket on right inside flap)

(FACEF)